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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/866,687	05/30/2001	Takeshi Misawa	0905-0261P	6060

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EXAMINER

YE, LIN

ART UNIT	PAPER NUMBER
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2622

DATE MAILED: 08/11/2006

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Application No.

09/866,687

Applicant(s)

MISAWA, TAKESHI

Examiner

Lin Ye

Art Unit

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --
Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 31 May 2006.
- 2a) ☒ This action is **FINAL**. 2b) ☐ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-8 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1-8 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☒ All b) ☐ Some * c) ☐ None of:
1. ☒ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. _____.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- * See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- 1) ☒ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☐ Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date _____.
- 4) ☐ Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____.
- 5) ☐ Notice of Informal Patent Application (PTO-152)
- 6) ☐ Other: _____.

DETAILED ACTION

Response to Arguments

1. Applicant's arguments with respect to claims 1-8 filed on 5/31/06 have been considered but are moot in view of the new ground(s) of rejection.

Claim Rejections - 35 USC § 102

2. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

3. Claims 1-8 are rejected under 35 U.S.C. 102(e) as being anticipated by Oda et al. U.S. Patent 6,795,119.

Referring to claim 1, the Oda reference discloses in Figures 2, 4a-4h and 5, a solid-state electronic imaging device (30) comprising: a lot of photoelectric conversion elements arranged in the column direction and the row direction (See Col. 10, lines 51-67); one or more vertical transfer paths (VR) for transferring signal charges respectively accumulated in said photoelectric conversion elements in the vertical direction; one or more transfer gates

for respectively shifting the signal charges accumulated in the photoelectric conversion elements to said vertical transfer paths (see Col. 11, lines 11-27); a horizontal transfer path (HR) for horizontally transferring the signal charges transferred from the vertical transfer paths; color filters (28) respectively formed on the photoelectric conversion elements and arranged such that the order of color signal components respectively represented by the signal charges substantially corresponding to one row which are inputted to the horizontal transfer path in reading out all pixels is a repetition of a red signal component, a green signal component, a blue signal component, and a green signal component, and the respective timings at which the red signal component and the blue signal component are outputted in odd rows are reverse to those in even rows; and a readout control (timing signal feeding section 22) for applying the transfer gate pulses to said transfer gates such that the order of color signal components respectively represented by the signal charges substantially corresponding to one row which are inputted to the horizontal transfer path is a repetition of a red signal component, a green signal component, a blue signal component, and a green signal component in every other row, and the respective timings at which the red signal component and the blue signal component are outputted in odd rows are reverse to those in even rows as shown in Figure 2 (See Col.11, lines 28-49); and wherein said horizontal transfer path is provided with a plurality of horizontal transfer electrodes (e.g., six-electrode structure); said solid-state electronic imaging device further comprising a mix control that applies horizontal transfer pulses to plural horizontal transfer electrodes of said horizontal transfer electrodes and changes the horizontal transfer electrodes to which said horizontal transfer pulses are applied in order to control mixing of the signal charges and an amount of

the mixed signal charges as shown in Figure 4 (See Col. 11, lines 50-67 and Col. 12, lines 1-8).

Referring to claim 2, the Oda reference discloses all subject matter as discussed in respected claim 1, and the Oda reference discloses photoelectric conversion elements are in a honeycomb arrangement as shown in Figure 2 where they are arranged in odd rows or even rows with respect to odd columns and arranged in even rows or odd rows with respect to even columns, and the color filters (28) which allow the transmission of a green light component are respectively arranged in said photoelectric conversion elements in odd rows or even rows, and the color filters which allow the transmission of a blue or red light component are alternately arranged for each column and for each row in said photoelectric conversion elements in even rows or odd rows.

Referring to claim 3, the Oda reference discloses all subject matter as discussed in respected claim 1, and the Oda reference discloses wherein said color filters are in a G-stripe R/B checkered arrangement where the color filters which allow the transmission of a green light component are arranged in a vertical stripe shape, and the color filters which allow the transmission of a blue or red light component are arranged in a checkered shape as shown in Figure 2.

Referring to claim 4, the Oda reference discloses all subject matter as discussed in respected with same comments to claim 1.

Referring to claim 5, the Oda reference discloses all subject matter as discussed in respected with same comments to claim 1.

Referring to claim 6, the Oda reference discloses all subject matter as discussed in respected claim 1, and the Oda reference discloses wherein every three adjacent signal charges from the row of signal charges inputted to the horizontal transfer path are mixed in the horizontal transfer path (See Col. 11, lines 64-67 and Col. 12, lines 1-8).

Referring to claim 7, the Oda reference discloses all subject matter as discussed in respected with same comments to claim 6.

Referring to claim 8, the Oda reference discloses all subject matter as discussed in respected with same comments to claim 6.

Conclusion

4. Applicant's amendment necessitated the new ground(s) of rejection presented in this Office action. Accordingly, **THIS ACTION IS MADE FINAL**. See MPEP § 706.07(a). Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire **THREE MONTHS** from the mailing date of this action. In the event a first reply is filed within **TWO MONTHS** of the mailing date of this final action and the advisory action is not mailed until after the end of the **THREE-MONTH** shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than **SIX MONTHS** from the date of this final action.

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5. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Lin Ye whose telephone number is (571) 272-7372. The examiner can normally be reached on Mon-Fri 8:00AM-5:00PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, David L. Ometz can be reached on (571) 272-7593. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.



Lin Ye
Primary Examiner
Art Unit 2622

August 8, 2006